

STM32系列 32位微控制器(MCU)



产品选型手册





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32位MCU - ARM® Cortex®-M内核



● Cortex-M0+无线协处理器

STM32 F0系列 – ARM® Cortex® -M0入门级MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IOInb | VinIn | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMP2C | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMAC | | | | | | | | | |
|------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|-------|-------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|-------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|------|---|---|---|---|---|---|---|---|---|
| STM32F0x0超值型 – 48 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F030F4P6 | 48 | ARM Cortex-M0 | 16 | 4 | 0 | TSSOP20 | 15 | 2.4 | 3.6 | 5 | 0 | 1 | 0 | 0 | 1 | 11 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | | | | | | |
| STM32F030K6T6 | 48 | ARM Cortex-M0 | 32 | 4 | 0 | LQFP32 | 26 | 2.4 | 3.6 | 5 | 0 | 1 | 0 | 0 | 1 | 12 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | | | | |
| STM32F030C6T6 | 48 | ARM Cortex-M0 | 32 | 4 | 0 | LQFP48 | 39 | 2.4 | 3.6 | 5 | 0 | 1 | 0 | 0 | 1 | 12 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | | | |
| STM32F030C8T6 | 48 | ARM Cortex-M0 | 64 | 8 | 0 | LQFP48 | 39 | 2.4 | 3.6 | 7 | 0 | 1 | 0 | 0 | 1 | 12 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | | |
| STM32F030CCT6 | 48 | ARM Cortex-M0 | 256 | 32 | 0 | LQFP48 | 37 | 2.4 | 3.6 | 8 | 0 | 1 | 0 | 0 | 1 | 12 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 2 | 0 | 6 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | |
| STM32F030R8T6 | 48 | ARM Cortex-M0 | 64 | 8 | 0 | LQFP64 | 55 | 2.4 | 3.6 | 7 | 0 | 1 | 0 | 0 | 1 | 18 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | |
| STM32F030RCT6 | 48 | ARM Cortex-M0 | 256 | 32 | 0 | LQFP64 | 51 | 2.4 | 3.6 | 8 | 0 | 1 | 0 | 0 | 1 | 18 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 2 | 0 | 6 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | |
| STM32F070F6P6 | 48 | ARM Cortex-M0 | 32 | 6 | 0 | TSSOP20 | 15 | 2.4 | 3.6 | 5 | 0 | 1 | 0 | 0 | 1 | 9 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | |
| STM32F070C6T6 | 48 | ARM Cortex-M0 | 32 | 6 | 0 | LQFP48 | 37 | 2.4 | 3.6 | 5 | 0 | 1 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32F070CBT6 | 48 | ARM Cortex-M0 | 128 | 16 | 0 | LQFP48 | 37 | 2.4 | 3.6 | 8 | 0 | 1 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 2 | 0 | 4 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32F070RBT6 | 48 | ARM Cortex-M0 | 128 | 16 | 0 | LQFP64 | 51 | 2.4 | 3.6 | 8 | 0 | 1 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 2 | 0 | 4 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32F0x1入门型 – 48 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F031F4P6 | 48 | ARM Cortex-M0 | 16 | 4 | 0 | TSSOP20 | 15 | 2 | 3.6 | 5 | 1 | 1 | 0 | 0 | 1 | 9 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32F031G4U6 | 48 | ARM Cortex-M0 | 16 | 4 | 0 | UFQFPN28 | 23 | 2 | 3.6 | 5 | 1 | 1 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32F031K4U6 | 48 | ARM Cortex-M0 | 16 | 4 | 0 | UFQFPN32 | 27 | 2 | 3.6 | 5 | 1 | 1 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32F031K4T6 | 48 | ARM Cortex-M0 | 16 | 4 | 0 | LQFP32 | 25 | 2 | 3.6 | 5 | 1 | 1 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32F031C4T6 | 48 | ARM Cortex-M0 | 16 | 4 | 0 | LQFP48 | 39 | 2 | 3.6 | 5 | 1 | 1 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32F031F6P6 | 48 | ARM Cortex-M0 | 32 | 4 | 0 | TSSOP20 | 15 | 2 | 3.6 | 5 | 1 | 1 | 0 | 0 | 1 | 9 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F031E6Y6 | 48 | ARM Cortex-M0 | 32 | 4 | 0 | WLCSQP25 | 20 | 2 | 3.6 | 5 | 1 | 1 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

STM32 F1系列 – ARM® Cortex®-M3基础型MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | E2prom (Bytes) | Package Name | IOInb | VinIn | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPP2C | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMAC | | | | | | | |
|------------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|-------|-------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|------|---|---|---|---|---|---|---|
| STM32F105 / 107连接型 - 72 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F105R8T6 | 72 | ARM Cortex-M3 | 64 | 64 | 0 | LQFP64 | 51 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | | |
| STM32F105RBT6 | 72 | ARM Cortex-M3 | 128 | 64 | 0 | LQFP64 | 51 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32F105RCT6 | 72 | ARM Cortex-M3 | 256 | 64 | 0 | LQFP64 | 51 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32F105V8T6 | 72 | ARM Cortex-M3 | 64 | 64 | 0 | LQFP100 | 80 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32F105VBH6 | 72 | ARM Cortex-M3 | 128 | 64 | 0 | LFPGA100 | 80 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32F105VBT6 | 72 | ARM Cortex-M3 | 128 | 64 | 0 | LQFP100 | 80 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32F105VCT6 | 72 | ARM Cortex-M3 | 256 | 64 | 0 | LQFP100 | 80 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32F107RBT6 | 72 | ARM Cortex-M3 | 128 | 64 | 0 | LQFP64 | 51 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 1 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32F107RCT6 | 72 | ARM Cortex-M3 | 256 | 64 | 0 | LQFP64 | 51 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 1 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F107VBT6 | 72 | ARM Cortex-M3 | 128 | 64 | 0 | LQFP100 | 80 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 1 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F107VCT6 | 72 | ARM Cortex-M3 | 256 | 64 | 0 | LQFP100 | 80 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 1 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F107VCH6 | 72 | ARM Cortex-M3 | 256 | 64 | 0 | LFPGA100 | 80 | 2 | 3.6 | 7 | 0 | 1 | 0 | 0 | 2 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 1 | 0 | 3+2 | 0 | 2 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

STM32 F2系列 – ARM® Cortex®-M3高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IONb | VinIn | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPC | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HNAC | |
|-------------------------|-----------------|---------------|----------------|--------------|---------------|--------------|------|-------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|------|---|
| STM32F215VET6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 |
| STM32F215VGT6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 |
| STM32F215ZET6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 |
| STM32F215ZGT6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 |
| STM32F2x7基础型 - 120 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F207VCT6 | 120 | ARM Cortex-M3 | 256 | 128 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207VET6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207VFT6 | 120 | ARM Cortex-M3 | 768 | 128 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207VGT6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207ZCT6 | 120 | ARM Cortex-M3 | 256 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207ZET6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207ZFT6 | 120 | ARM Cortex-M3 | 768 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207ZGT6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207ICT6 | 120 | ARM Cortex-M3 | 256 | 128 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207IET6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207IFT6 | 120 | ARM Cortex-M3 | 768 | 128 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207IGT6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207ICH6 | 120 | ARM Cortex-M3 | 256 | 128 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32F207IEH6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 |

STM32 F2系列 – ARM® Cortex®-M3高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | E2prom (Bytes) | Package Name | IONb | Vin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMAC | | |
|-------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|-----|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|------|---|---|
| STM32F207IFH6 | 120 | ARM Cortex-M3 | 768 | 128 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F207IGH6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F217VET6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | | |
| STM32F217VGT6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | | |
| STM32F217ZET6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | |
| STM32F217ZGT6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | |
| STM32F217IET6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | |
| STM32F217IGT6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | |
| STM32F217IEH6 | 120 | ARM Cortex-M3 | 512 | 128 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 |
| STM32F217IGH6 | 120 | ARM Cortex-M3 | 1024 | 128 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 2 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | E2prom (Bytes) | Package Name | IONb | Vinb | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMAC | | | |
|-----------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|------|---|---|---|
| STM32F412入门型 - 100 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F412CEU6 | 100 | ARM Cortex-M4 | 512 | 256 | 0 | UFQFPN48 | 36 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 5 | 0 | 5 | 4 | [1] | 3 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | | |
| STM32F412RET6 | 100 | ARM Cortex-M4 | 512 | 256 | 0 | LQFP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | | | | |
| STM32F412REV6 | 100 | ARM Cortex-M4 | 512 | 256 | 0 | WLCSP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | | | | |
| STM32F412VET6 | 100 | ARM Cortex-M4 | 512 | 256 | 0 | LQFP100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F412VEH6 | 100 | ARM Cortex-M4 | 512 | 256 | 0 | UFBGA100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | |
| STM32F412ZET6 | 100 | ARM Cortex-M4 | 512 | 256 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | |
| STM32F412ZEH6 | 100 | ARM Cortex-M4 | 512 | 256 | 0 | UFBGA144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | |
| STM32F412CGU6 | 100 | ARM Cortex-M4 | 1024 | 256 | 0 | UFQFPN48 | 36 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 10 | 0 | 0 | 0 | 0 | 0 | 5 | 0 | 5 | 4 | [1] | 3 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F412RGT6 | 100 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | | |
| STM32F412RGY6 | 100 | ARM Cortex-M4 | 1024 | 256 | 0 | WLCSP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | |
| STM32F412VGT6 | 100 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | | |
| STM32F412VGH6 | 100 | ARM Cortex-M4 | 1024 | 256 | 0 | UFBGA100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | |
| STM32F412ZGT6 | 100 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | |
| STM32F412ZGH6 | 100 | ARM Cortex-M4 | 1024 | 256 | 0 | UFBGA144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 1 | 16 | 0 | 0 | 0 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | |
| STM32F413/423入门型 - 100 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F413CGU6 | 100 | ARM Cortex-M4 | 1024 | 320 | 0 | UFQFPN48 | 36 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 0 | 0 | 5 | 0 | 5 | 4 | [1] | 3+3 | 0 | 3 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F413RGT6 | 100 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+3 | 0 | 3 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F413VGT6 | 100 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F413VGJ6 | 100 | ARM Cortex-M4 | 1024 | 320 | 0 | UFBGA100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPC2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MIPIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMxAC | | | | |
|------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|--------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|-------|---|---|---|---|
| STM32F413ZGT6 | 100 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F413ZGJ6 | 100 | ARM Cortex-M4 | 1024 | 320 | 0 | UFPGA144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F413CHU6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | UFQFPN48 | 36 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 0 | 0 | 5 | 0 | 5 | 4 | [1] | 3+3 | 0 | 3 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | |
| STM32F413RHT6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | LQFP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+3 | 0 | 3 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | |
| STM32F413VHT6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | LQFP100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | |
| STM32F413VHJ6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | UFPGA100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F413ZHT6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F413ZHJ6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | UFPGA144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F423CHU6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | UFQFPN48 | 36 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 0 | 0 | 5 | 0 | 5 | 4 | [1] | 3+3 | 0 | 3 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32F423RHT6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | LQFP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+3 | 0 | 3 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32F423VHT6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | LQFP100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32F423VHJ6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | UFPGA100 | 81 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32F423ZHT6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32F423ZHJ6 | 100 | ARM Cortex-M4 | 1536 | 320 | 0 | UFPGA144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 5 | 4 | [1] | 4+6 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32F405 / 415基本型 - 168 Mhz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F405RGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP64 | 51 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| STM32F405OGY6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | WLCSP90 | 72 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 13 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| STM32F405OY6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | WLCSP90 | 72 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 13 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| STM32F405VGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| STM32F405ZGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMxAC | | | | | |
|------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|-------|---|---|---|---|---|
| STM32F415RG6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP64 | 51 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | | | |
| STM32F4150G6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | WLCSP90 | 72 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 13 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | | |
| STM32F415VGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | |
| STM32F415ZGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | |
| STM32F407 / 417基本型 - 168 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F407VET6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F407VGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F407ZET6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F407ZGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F407IET6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F407IEH6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | UFPGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F407IGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F407IGH6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | UFPGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F417VET6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F417VGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F417ZET6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F417ZGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F417IET6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F417IEH6 | 168 | ARM Cortex-M4 | 512 | 192 | 0 | UFPGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F417IGT6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vinb | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMAC | | |
|-------------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|------|---|---|
| STM32F417IGH6 | 168 | ARM Cortex-M4 | 1024 | 192 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 3 | 0 | 2 | 3 | 0 | 4+2 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F446基本型 - 180 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F446MCY6 | 180 | ARM Cortex-M4 | 256 | 128 | 0 | WLCS81 | 63 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 14 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F446MEY6 | 180 | ARM Cortex-M4 | 512 | 128 | 0 | WLCS81 | 63 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 14 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32F446RCT6 | 180 | ARM Cortex-M4 | 256 | 128 | 0 | LQFP64 | 50 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F446RET6 | 180 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP64 | 50 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F446VCT6 | 180 | ARM Cortex-M4 | 256 | 128 | 0 | LQFP100 | 81 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 2 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F446VET6 | 180 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP100 | 81 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F446ZCT6 | 180 | ARM Cortex-M4 | 256 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F446ZCH6 | 180 | ARM Cortex-M4 | 256 | 128 | 0 | UFBGA144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F446ZET6 | 180 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F446ZEH6 | 180 | ARM Cortex-M4 | 512 | 128 | 0 | UFBGA144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [1] | 4+2 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32F427 / 437高级型 - 180 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F427VGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F427VIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F427ZGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F427ZIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F427AGH6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | UFBGA169 | 130 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F427AIH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | UFBGA169 | 130 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F427IGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IONb | Vinb | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMAC | | | |
|-------------------------------------|-----------------|---------------|----------------|--------------|---------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|------|---|---|---|
| STM32F427IGH6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32F427IIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F427IHH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | UFBGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F437VGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F437VIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F437ZGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F437ZIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F437AIH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | UFBGA169 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F437IGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F437IGH6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F437IIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F437IHH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F429 / 439高级型 - 180 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F429VET6 | 180 | ARM Cortex-M4 | 512 | 256 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F429VGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F429VIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP100 | 82 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F429ZET6 | 180 | ARM Cortex-M4 | 512 | 256 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F429ZGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F429ZIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP144 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F429ZEY6 | 180 | ARM Cortex-M4 | 512 | 256 | 0 | WLCSP143 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MIPIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMxAC |
|-------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|--------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|-------|
| STM32F429ZGY6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | WLCSP143 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | |
| STM32F429ZIY6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | WLCSP143 | 114 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429AGH6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | UFPGA169 | 130 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429AIH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | UFPGA169 | 130 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429IEH6 | 180 | ARM Cortex-M4 | 512 | 256 | 0 | UFPGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429IET6 | 180 | ARM Cortex-M4 | 512 | 256 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429IGH6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | UFPGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429IGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429IHH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | UFPGA176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429IIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP176 | 140 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429IBET6 | 180 | ARM Cortex-M4 | 512 | 256 | 0 | LQFP208 | 168 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429IBGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP208 | 168 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429BIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP208 | 168 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429NEH6 | 180 | ARM Cortex-M4 | 512 | 256 | 0 | TFBGA216 | 168 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429NGH6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | TFBGA216 | 168 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F429NIH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | TFBGA216 | 168 | 1.8 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 |
| STM32F439VGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 |
| STM32F439VIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 |
| STM32F439AIH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | UFPGA169 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IONb | Vinb | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MIPIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMxAC | |
|------------------------------|-----------------|---------------|----------------|--------------|---------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|--------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|-------|---|
| STM32F439ZGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439ZIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439ZIY6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | WLCSP143 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439IGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439IGH6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | UFPGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439IIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439IHH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | UFPGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439BGT6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | LQFP208 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439BIT6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | LQFP208 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439NGH6 | 180 | ARM Cortex-M4 | 1024 | 256 | 0 | TFBGA216 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F439NIH6 | 180 | ARM Cortex-M4 | 2048 | 256 | 0 | TFBGA216 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 0 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F469 / 479高级型 - 180 Mhz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F469AEH6 | 180 | ARM Cortex-M4 | 512 | 384 | 0 | UFPGA169 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F469AGH6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | UFPGA169 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F469AIH6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | UFPGA169 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F469AEY6 | 180 | ARM Cortex-M4 | 512 | 384 | 0 | WLCSP168 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F469AGY6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | WLCSP168 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F469AIY6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | WLCSP168 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F469IET6 | 180 | ARM Cortex-M4 | 512 | 384 | 0 | LQFP176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F469IEH6 | 180 | ARM Cortex-M4 | 512 | 384 | 0 | UFPGA176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MIPIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMxAC | | | | | |
|-------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|--------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|-------|---|---|---|---|---|
| STM32F469IGT6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | LQFP176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469IGH6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | UFPGA176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469IIT6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | LQFP176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469IIH6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | UFPGA176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469IBET6 | 180 | ARM Cortex-M4 | 512 | 384 | 0 | LQFP208 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469IBGT6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | LQFP208 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469BIT6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | LQFP208 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469NEH6 | 180 | ARM Cortex-M4 | 512 | 384 | 0 | TFBGA216 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469NGH6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | TFBGA216 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F469NIH6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | TFBGA216 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | | |
| STM32F479AGH6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | UFPGA169 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | | |
| STM32F479AIH6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | UFPGA169 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | |
| STM32F479AGY6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | WLCSP168 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | |
| STM32F479AY6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | WLCSP168 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 |
| STM32F479IGT6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | LQFP176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 |
| STM32F479IGH6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | UFPGA176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 |
| STM32F479IIT6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | LQFP176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 |
| STM32F479IIH6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | UFPGA176 | 131 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 |
| STM32F479BGT6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | LQFP208 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 |

STM32 F4系列 – ARM® Cortex®-M4高性能MCU(附带DSP和FPU)

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | E2prom (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMP2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMAC | | | | |
|-------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|-------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|------|---|---|---|---|
| STM32F479BIT6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | LQFP208 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | | |
| STM32F479NGH6 | 180 | ARM Cortex-M4 | 1024 | 384 | 0 | TFBGA216 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| STM32F479NIH6 | 180 | ARM Cortex-M4 | 2048 | 384 | 0 | TFBGA216 | 161 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 2 | 3 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |

STM32 F7系列 – ARM® Cortex® -M7高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSH0ST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMAC | |
|-------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|------|---|
| STM32F7x2基础型 - 216 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F722RCT6 | 216 | ARM Cortex-M7 | 256 | 256+16 | 0 | LQFP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 1 | 3 | 3 | [3] | 4+2 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F722RET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 1 | 3 | 3 | [3] | 4+2 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F722VCT6 | 216 | ARM Cortex-M7 | 256 | 256+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F722VET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F722ZCT6 | 216 | ARM Cortex-M7 | 256 | 256+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F722ZET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F722IET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F722IEK6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F732RET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP64 | 50 | 1.7 | 3.6 | 12 | 2 | 2 | 0 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 3 | 1 | 3 | 3 | [3] | 4+2 | 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32F732VET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32F732ZET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32F732IET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32F732IEK6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | | |
| STM32F7x3基础型 - 216 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F723VEY6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | WLCS100 | 79 | 1.7 | 3.6 | 13 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F723ZCT6 | 216 | ARM Cortex-M7 | 256 | 256+16 | 0 | LQFP144 | 112 | 1.7 | 3.6 | 13 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F723ZC16 | 216 | ARM Cortex-M7 | 256 | 256+16 | 0 | UFBGA144 | 112 | 1.7 | 3.6 | 13 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F723ZET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP144 | 112 | 1.7 | 3.6 | 13 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F723ZE16 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | UFBGA144 | 112 | 1.7 | 3.6 | 13 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | | |

STM32 F7系列 – ARM® Cortex®-M7高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IOInb | VinIn | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer (16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MIPIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMxAC | | |
|-------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|-------|-------|------|-------------------|-------------------|---------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|--------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|-------|---|---|
| STM32F723ICT6 | 216 | ARM Cortex-M7 | 256 | 256+16 | 0 | LQFP176 | 138 | 1.7 | 3.6 | 14 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F723ICK6 | 216 | ARM Cortex-M7 | 256 | 256+16 | 0 | UFPGA176 | 138 | 1.7 | 3.6 | 14 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F723IET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP176 | 138 | 1.7 | 3.6 | 14 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F723IEK6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | UFPGA176 | 138 | 1.7 | 3.6 | 14 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F733VEY6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | WLSP100 | 79 | 1.7 | 3.6 | 13 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32F733ZET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP144 | 112 | 1.7 | 3.6 | 13 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32F733ZEI6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | UFPGA144 | 112 | 1.7 | 3.6 | 13 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32F733IET6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | LQFP176 | 138 | 1.7 | 3.6 | 14 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32F733IEK6 | 216 | ARM Cortex-M7 | 512 | 256+16 | 0 | UFPGA176 | 138 | 1.7 | 3.6 | 14 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 5 | 1 | 3 | 3 | [3] | 4+4 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32F7x5高级型 - 216 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F745VET6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745VGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745VEH6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | TFBGA100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745VGH6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | TFBGA100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745ZET6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745ZGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745ZEY6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | WLSP143 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745ZGY6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | WLSP143 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745IET6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745IGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |

STM32 F7系列 – ARM® Cortex® -M7高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | VinIn | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMxAC | | |
|-------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|-------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|-------|--|--|
| STM32F745IEK6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F745IGK6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765VGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765VIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765ZGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765ZIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765IGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765IIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765IGK6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765IHK6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765BGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP208 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765BIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP208 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765NGH6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | TFBGA216 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F765NIH6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | TFBGA216 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 3 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F7x6高级型 - 216 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F746VET6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F746VGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F746VEH6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | TFBGA100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32F746VGH6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | TFBGA100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | | |

STM32 F7系列 – ARM® Cortex®-M7高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IONb | Vinb | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMAC |
|-------------------------|-----------------|---------------|----------------|--------------|---------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|------|
| STM32F746ZET6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746ZGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746ZEY6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | WLCSP143 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746ZGY6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | WLCSP143 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746IET6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746IGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746IEK6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746IGK6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746BET6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | LQFP208 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746BGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP208 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746NEH6 | 216 | ARM Cortex-M7 | 512 | 320+16 | 0 | TFBGA216 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F746NGH6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | TFBGA216 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F756VGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F756ZGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F756ZGY6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | WLCSP143 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F756IGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F756IGK6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | UFBGA176 | 140 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F756BGT6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | LQFP208 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F756NGH6 | 216 | ARM Cortex-M7 | 1024 | 320+16 | 0 | TFBGA216 | 168 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | 0 | 4+4 | 0 | 2 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 2 | 4 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |

STM32 F7系列 – ARM® Cortex® -M7高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMxAC |
|-------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|-------|
| STM32F7x7高级型 - 216 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F767VGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767VIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767ZGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767ZIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767IGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767IIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767IGK6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | UFPGA176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767IHK6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | UFPGA176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767BGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP208 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767BIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP208 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767NGH6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | TFBGA216 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F767NIH6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | TFBGA216 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32F777VIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP100 | 82 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 16 | 0 | 0 | 2 | 0 | 0 | 4 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F777ZIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP144 | 114 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F777IIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F777IK6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | UFPGA176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F777BIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP208 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| STM32F777NIH6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | TFBGA216 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |

STM32 F7系列 – ARM® Cortex® -M7高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | E2prom (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMP2C | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMxAC | |
|-------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|-------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|-------|---|
| STM32F7x9高级型 - 216 Mhz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32F769AGY6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | WLCS180 | 129 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F769AY6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | WLCS180 | 129 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F769BGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP208 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F769BIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP208 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F769IGT6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | LQFP176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F769IIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F769NGH6 | 216 | ARM Cortex-M7 | 1024 | 512+16 | 0 | TFBGA216 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F769NIH6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | TFBGA216 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32F779AIY6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | WLCS180 | 129 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 |
| STM32F779BIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP208 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F779IIT6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | LQFP176 | 132 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |
| STM32F779NIH6 | 216 | ARM Cortex-M7 | 2048 | 512+16 | 0 | TFBGA216 | 159 | 1.7 | 3.6 | 12 | 2 | 2 | 1 | 0 | 3 | 24 | 0 | 0 | 2 | 0 | 0 | 6 | 1 | 3 | 4 | [4] | 4+4 | 0 | 3 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 2 | 4 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | |

STM32 H7系列 – ARM® Cortex®-M7超高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | E2prom (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMAC |
|-------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|------|
| STM32H7x3产品线 - 400 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32H743VIT6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32H743ZIT6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | LQFP144 | 114 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32H743AI6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | UFBGA169 | 131 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32H743IIT6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | LQFP176 | 140 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32H743BIT6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | LQFP208 | 168 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32H743XIH6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | TFBGA265 | 168 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32H753VIT6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| STM32H753ZIT6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | LQFP144 | 114 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| STM32H753AI6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | UFBGA169 | 131 | 1.71 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| STM32H753IIT6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | LQFP176 | 140 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| STM32H753BIT6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | LQFP208 | 168 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |
| STM32H753XIH6 | 400 | ARM Cortex-M7 | 2048 | 1024 | 0 | TFBGA265 | 168 | 1.62 | 3.6 | 14 | 2 | 2 | 5 | 1 | 0 | [20] | 3 | 20 | 2 | 2 | 2 | 6 | 1 | 3 | 4 | [4] | 4+4 | 1 | 2 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 4 | 4 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | |

STM32 L0系列 – ARM® Cortex® -M0+超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cells | Nb ADC 12-bit Channels | Nb ADC 16-bit Cells | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | USART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMxAC | |
|-------------------------|-----------------|----------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|---------------------|------------------------|---------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|-------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|-------|---|
| STM32L0x1入门型 - 32 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L011D3P6 | 32 | ARM Cortex-M0+ | 8 | 2 | 512 | TSSOP14 | 11 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 4 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L011D4P6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | TSSOP14 | 11 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 4 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L011F3P6 | 32 | ARM Cortex-M0+ | 8 | 2 | 512 | TSSOP20 | 16 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 9 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L011F3U6 | 32 | ARM Cortex-M0+ | 8 | 2 | 512 | UFQFPN20 | 16 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 7 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L011F4P6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | TSSOP20 | 16 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 9 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L011F4U6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | UFQFPN20 | 16 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 7 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L011E3Y6 | 32 | ARM Cortex-M0+ | 8 | 2 | 512 | WLCSP25 | 21 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L011E4Y6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | WLCSP25 | 21 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L011G3U6 | 32 | ARM Cortex-M0+ | 8 | 2 | 512 | UFQFPN28 | 24 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L011G4U6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | UFQFPN28 | 24 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L011K3T6 | 32 | ARM Cortex-M0+ | 8 | 2 | 512 | LQFP32 | 26 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L011K3U6 | 32 | ARM Cortex-M0+ | 8 | 2 | 512 | UFQFPN32 | 28 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L011K4T6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | LQFP32 | 26 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L011K4U6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | UFQFPN32 | 28 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L021D4P6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | TSSOP14 | 11 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 4 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L021F4P6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | TSSOP20 | 16 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 9 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L021F4U6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | UFQFPN20 | 16 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 7 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L021G4U6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | UFQFPN28 | 24 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

STM32 L0系列 – ARM® Cortex® -M0+超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIODS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMxAC | | | | | | | | |
|-------------------------|-----------------|----------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|--------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|-------|---|---|---|---|---|---|---|---|
| STM32L021K4T6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | LQFP32 | 26 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | | | | | | | | |
| STM32L021K4U6 | 32 | ARM Cortex-M0+ | 16 | 2 | 512 | UFQFPN32 | 28 | 1.65 | 3.6 | 2 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | | | | | |
| STM32L031F4P6 | 32 | ARM Cortex-M0+ | 16 | 8 | 1024 | TSSOP20 | 15 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | |
| STM32L031F6P6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | TSSOP20 | 15 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | |
| STM32L031E4Y6 | 32 | ARM Cortex-M0+ | 16 | 8 | 1024 | WLCS25 | 20 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | |
| STM32L031E6Y6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | WLCS25 | 20 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32L031G4U6 | 32 | ARM Cortex-M0+ | 16 | 8 | 1024 | UFQFPN28 | 21 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32L031G6U6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | UFQFPN28 | 21 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32L031K4T6 | 32 | ARM Cortex-M0+ | 16 | 8 | 1024 | LQFP32 | 25 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L031K4U6 | 32 | ARM Cortex-M0+ | 16 | 8 | 1024 | UFQFPN32 | 27 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L031K6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | LQFP32 | 25 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L031K6U6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | UFQFPN32 | 27 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L031C6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | LQFP48 | 38 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L041E6Y6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | WLCS25 | 20 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | | | |
| STM32L041F6P6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | TSSOP20 | 15 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L041G6U6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | UFQFPN28 | 21 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L041K6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | LQFP32 | 25 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L041K6U6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | UFQFPN32 | 27 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L041C6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 1024 | LQFP48 | 38 | 1.65 | 3.6 | 3 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

STM32 L0系列 – ARM® Cortex® -M0+超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMxAC | | | | | | | |
|-------------------------|-----------------|----------------|----------------|--------------|---------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|-------|---|---|---|---|---|---|---|
| STM32L051K6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | LQFP32 | 25 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | | | |
| STM32L051K6U6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | UFQFPN32 | 27 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L051K8T6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | LQFP32 | 25 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L051K8U6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | UFQFPN32 | 27 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 1 | [1] | 2 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L051T6Y6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | WLCSFP36 | 29 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L051T8Y6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | WLCSFP36 | 29 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L051C6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | LQFP48 | 37 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L051C8T6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | LQFP48 | 37 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L051R6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | LQFP64 | 51 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 0 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L051R6H6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | TFBGA64 | 51 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 0 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L051R8T6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | LQFP64 | 51 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 0 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L051R8H6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | TFBGA64 | 51 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 0 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L071K8U6 | 32 | ARM Cortex-M0+ | 64 | 20 | 3072 | UFQFPN32 | 23 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L071KBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP32 | 25 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L071KBU6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | UFQFPN32 | 23 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L071KZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP32 | 25 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L071KZU6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | UFQFPN32 | 23 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L071C8T6 | 32 | ARM Cortex-M0+ | 64 | 20 | 3072 | LQFP48 | 37 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 13 | 0 | 0 | 0 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L071CBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP48 | 37 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 13 | 0 | 0 | 0 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

STM32 L0系列 – ARM® Cortex® -M0+ 超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer (16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPC | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMAC | | |
|---------------------------------|-----------------|----------------|----------------|--------------|---------------|--------------|------|------|------|-------------------|-------------------|---------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|------|-------------|---------|--------|------|---------|-------|------|-------|------|-----|---------|-----|------|---|---|
| STM32L072CZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP48 | 37 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L072CZY6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | WLCSP49 | 40 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 13 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L072RBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP64 | 51 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072RBH6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | TFBGA64 | 50 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 15 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072RZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP64 | 51 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072RZH6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | TFBGA64 | 50 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 15 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072RZL6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | UFBGA64 | 51 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072V8T6 | 32 | ARM Cortex-M0+ | 64 | 20 | 3072 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L072V8I6 | 32 | ARM Cortex-M0+ | 64 | 20 | 3072 | UFBGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072VBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072VBI6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | UFBGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072VZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L072VZI6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | UFBGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L082KBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP32 | 25 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L082KBU6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | UFQFPN32 | 23 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L082KZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP32 | 25 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L082KZU6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | UFQFPN32 | 23 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 1 | 0 | 0 | 2 | [2] | 3 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L082CZY6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | WLCSP49 | 40 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 13 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L0x3 USB & LCD功能型 - 32 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L053C6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | LQFP48 | 37 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 1 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 4x18 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |

STM32 L0系列 – ARM® Cortex® -M0+ 超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer (16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMCC | | |
|-------------------------|-----------------|----------------|----------------|--------------|---------------|--------------|------|------|------|-------------------|-------------------|---------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|-----------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|------|---|---|
| STM32L053C8T6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | LQFP48 | 37 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 1 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 4x18 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L053R6T6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | LQFP64 | 51 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L053R6H6 | 32 | ARM Cortex-M0+ | 32 | 8 | 2048 | TFBGA64 | 50 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 15 | 0 | 0 | 1 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L053R8T6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | LQFP64 | 51 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L053R8H6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | TFBGA64 | 50 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 15 | 0 | 0 | 1 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L063C8T6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | LQFP48 | 37 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 1 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x18 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
| STM32L063R8T6 | 32 | ARM Cortex-M0+ | 64 | 8 | 2048 | LQFP64 | 51 | 1.65 | 3.6 | 4 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 0 | 2 | 0 | 1 | 2 | [2] | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
| STM32L073CBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP48 | 37 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x18 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073CZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP48 | 37 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x18 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073RBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP64 | 51 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073RBH6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | TFBGA64 | 50 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 15 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073RZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP64 | 51 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073RZH6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | TFBGA64 | 50 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 15 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073V8T6 | 32 | ARM Cortex-M0+ | 64 | 20 | 3072 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073V8H6 | 32 | ARM Cortex-M0+ | 64 | 20 | 3072 | UFPGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073VBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 15 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073VBH6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | UFPGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073VZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| STM32L073VZH6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | UFPGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 15 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |

STM32 L0系列 – ARM® Cortex® -M0+ 超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IO nb | V _{in} | V _{max} | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer (16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMAC |
|-------------------------|-----------------|----------------|----------------|--------------|---------------|--------------|-------|-----------------|------------------|-------------------|-------------------|---------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|-----------|-----------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|------|
| STM32L083CBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP48 | 40 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 4x18 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
| STM32L083CZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP48 | 40 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 4x18 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
| STM32L083RBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP64 | 51 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L083RZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP64 | 51 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L083RZH6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | TFBGA64 | 51 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L083V8T6 | 32 | ARM Cortex-M0+ | 64 | 20 | 3072 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L083V8I6 | 32 | ARM Cortex-M0+ | 64 | 20 | 3072 | UFBGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L083VBT6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L083VBI6 | 32 | ARM Cortex-M0+ | 128 | 20 | 6144 | UFBGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L083VZT6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | LQFP100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L083VZI6 | 32 | ARM Cortex-M0+ | 192 | 20 | 6144 | UFBGA100 | 84 | 1.65 | 3.6 | 6 | 0 | 0 | 1 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 1 | 3 | [3] | 4 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x48/4x52 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | | |

STM32 L1系列 – ARM® Cortex®-M3超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMAC | | | | |
|------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|------|---|---|---|---|
| STM32L100超值型 - 32 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L100C8U6-A | 32 | ARM Cortex-M3 | 32 | 4 | 2048 | UFQFPN48 | 37 | 1.8 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x16 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32L100R8T6-A | 32 | ARM Cortex-M3 | 64 | 8 | 2048 | LQFP64 | 51 | 1.8 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 20 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32L100R8T6-A | 32 | ARM Cortex-M3 | 128 | 16 | 2048 | LQFP64 | 51 | 1.8 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 20 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L100RCT6 | 32 | ARM Cortex-M3 | 256 | 16 | 4096 | LQFP64 | 51 | 1.8 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 20 | 0 | 0 | 2 | 2 | 0 | 3 | 0 | 2 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L151入门型 - 32 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L151C6T6-A | 32 | ARM Cortex-M3 | 32 | 16 | 4096 | LQFP48 | 37 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L151C8U6-A | 32 | ARM Cortex-M3 | 32 | 16 | 4096 | UFQFPN48 | 37 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L151C8T6-A | 32 | ARM Cortex-M3 | 64 | 32 | 4096 | LQFP48 | 37 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L151C8U6-A | 32 | ARM Cortex-M3 | 64 | 32 | 4096 | UFQFPN48 | 37 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L151C8T6-A | 32 | ARM Cortex-M3 | 128 | 32 | 4096 | LQFP48 | 37 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L151C8U6-A | 32 | ARM Cortex-M3 | 128 | 32 | 4096 | UFQFPN48 | 37 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L151R6T6-A | 32 | ARM Cortex-M3 | 32 | 16 | 4096 | LQFP64 | 51 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 20 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L151R6H6-A | 32 | ARM Cortex-M3 | 32 | 16 | 4096 | TFBGA64 | 50 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 19 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L151R8T6-A | 32 | ARM Cortex-M3 | 64 | 32 | 4096 | LQFP64 | 51 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 20 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L151R8H6-A | 32 | ARM Cortex-M3 | 64 | 32 | 4096 | TFBGA64 | 50 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 19 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| STM32L151R8T6-A | 32 | ARM Cortex-M3 | 128 | 32 | 4096 | LQFP64 | 51 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 20 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L151R8H6-A | 32 | ARM Cortex-M3 | 128 | 32 | 4096 | TFBGA64 | 50 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 19 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| STM32L151V8T6-A | 32 | ARM Cortex-M3 | 64 | 32 | 4096 | LQFP100 | 83 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 24 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L151V8H6-A | 32 | ARM Cortex-M3 | 64 | 32 | 4096 | UFBGA100 | 83 | 1.65 | 3.6 | 8 | 0 | 0 | 0 | 0 | 1 | 24 | 0 | 0 | 2 | 2 | 0 | 2 | 0 | 0 | 2 | 0 | 3 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |

STM32 L4系列 – ARM® Cortex®-M4超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPC | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DHHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMxAC | | | | | | |
|------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|-------|---|---|---|---|---|---|
| STM32L451RCI6 | 80 | ARM Cortex-M4 | 256 | 160 | 0 | UFBGA64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 1 | 3 | 1 | 0 | 4 | [4] | 3+1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | | |
| STM32L451REV6 | 80 | ARM Cortex-M4 | 512 | 160 | 0 | WLCSPP64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 1 | 3 | 1 | 0 | 4 | [4] | 3+1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L451REI6 | 80 | ARM Cortex-M4 | 512 | 160 | 0 | UFBGA64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 1 | 3 | 1 | 0 | 4 | [4] | 3+1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L451RET6 | 80 | ARM Cortex-M4 | 512 | 160 | 0 | LQFP64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 1 | 3 | 1 | 0 | 4 | [4] | 3+1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L451VCT6 | 80 | ARM Cortex-M4 | 256 | 160 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 1 | 3 | 1 | 0 | 4 | [4] | 3+1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L451VCI6 | 80 | ARM Cortex-M4 | 256 | 160 | 0 | UFBGA100 | 83 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 1 | 3 | 1 | 0 | 4 | [4] | 3+1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L451VET6 | 80 | ARM Cortex-M4 | 512 | 160 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 1 | 3 | 1 | 0 | 4 | [4] | 3+1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | | |
| STM32L451VEI6 | 80 | ARM Cortex-M4 | 512 | 160 | 0 | UFBGA100 | 83 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 1 | 2 | 1 | 3 | 1 | 0 | 4 | [4] | 3+1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | | |
| STM32L471RET6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L471RGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L471VET6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32L471VGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32L471QEI6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | UFBGA132 | 109 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 3 | 19 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32L471QGI6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | UFBGA132 | 109 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 3 | 19 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L471ZET6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP144 | 114 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32L471ZGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP144 | 114 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4x2 USB FS产品线 - 80 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L432KBU6 | 80 | ARM Cortex-M4 | 128 | 64 | 0 | UFQFPN32 | 26 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 1 | 2 | 1 | 0 | 2 | [2] | 2 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32L432KCU6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | UFQFPN32 | 26 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 1 | 2 | 1 | 0 | 2 | [2] | 2 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | |
| STM32L442KCU6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | UFQFPN32 | 26 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 1 | 2 | 1 | 0 | 2 | [2] | 2 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |

STM32 L4系列 – ARM® Cortex®-M4超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IONb | Vinb | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPC | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | D/HOST | SAI | SPDIFRX | D/FSM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMCC | | |
|-------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|------|---|---|
| STM32L433RBY6 | 80 | ARM Cortex-M4 | 128 | 64 | 0 | WLCS64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L433RCT6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | LQFP64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L433RCI6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | UF64A64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L433RCY6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | WLCS64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L433VCT6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x44/8x40 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L433VCI6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | UF64A100 | 83 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x44/8x40 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | | |
| STM32L443CCT6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | LQFP48 | 38 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x19 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | | | |
| STM32L443CCU6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | UFQFPN48 | 38 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x19 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | | | |
| STM32L443CCY6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | WLCS49 | 39 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 10 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x19 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | | | |
| STM32L443RCT6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | LQFP64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L443RCI6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | UF64A64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L443RCY6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | WLCS64 | 52 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x32/8x28 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L443VCT6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x44/8x40 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L443VCI6 | 80 | ARM Cortex-M4 | 256 | 64 | 0 | UF64A100 | 83 | 1.71 | 3.6 | 5 | 1 | 1 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 1 | 3 | 1 | 0 | 3 | 0 | 3 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 4x44/8x40 | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 0 | 0 | | |
| STM32L4x5 USB OTG产品线 - 80 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L475RCT6 | 80 | ARM Cortex-M4 | 256 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | 0 | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L475RET6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | 0 | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L475RGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | 0 | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32L475VCT6 | 80 | ARM Cortex-M4 | 256 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | 0 | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 |
| STM32L475VET6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | 0 | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 |

STM32 L4系列 – ARM® Cortex®-M4超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Ezproc (Bytes) | Package Name | IONb | Vinb | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMCC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOCS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMCC |
|-------------------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|------|------------|------------|------------|----------|--------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|------|
| STM32L475VGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | 0 | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L4x6 USB OTG & LCD产品线 - 80 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L476RCT6 | 80 | ARM Cortex-M4 | 256 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476RET6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476RGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476JEY6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | WLCSF72 | 57 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476JGY6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | WLCSF72 | 57 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476MEY6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | WLCSF81 | 65 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x30/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476MGY6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | WLCSF81 | 65 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x30/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476VCT6 | 80 | ARM Cortex-M4 | 256 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476VET6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476VGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476QEI6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | UFBGA132 | 109 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 19 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476QGI6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | UFBGA132 | 109 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 19 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476ZET6 | 80 | ARM Cortex-M4 | 512 | 128 | 0 | LQFP144 | 114 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L476ZGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP144 | 114 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L486RGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP64 | 51 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 |
| STM32L486JGY6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | WLCSF72 | 57 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x28/4x32 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 |
| STM32L486VGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP100 | 82 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 |
| STM32L486QGI6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | UFBGA132 | 109 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 19 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 |

STM32 L4系列 – ARM® Cortex®-M4超低功耗MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | E2prom (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIODS | Segment LCD | TFT LCD | DH0ST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMAC |
|-------------------------|-----------------|---------------|----------------|--------------|----------------|--------------|------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|--------|-------------|---------|-------|-----|---------|-------|------|-------|------|-----|---------|-----|------|
| STM32L486ZGT6 | 80 | ARM Cortex-M4 | 1024 | 128 | 0 | LQFP144 | 114 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 3 | [3] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 0 | 0 |
| STM32L496RET6 | 80 | ARM Cortex-M4 | 512 | 320 | 0 | LQFP64 | 52 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496RGT6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP64 | 52 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496VET6 | 80 | ARM Cortex-M4 | 512 | 320 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496VGT6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496VGY6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | WLCSP100 | 83 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496QEI6 | 80 | ARM Cortex-M4 | 512 | 320 | 0 | UFBGA132 | 110 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 19 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496QGI6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | UFBGA132 | 110 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 19 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496ZET6 | 80 | ARM Cortex-M4 | 512 | 320 | 0 | LQFP144 | 115 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496ZGT6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP144 | 115 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496AEI6 | 80 | ARM Cortex-M4 | 512 | 320 | 0 | UFBGA169 | 136 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L496AGI6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | UFBGA169 | 136 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| STM32L4A6RGT6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP64 | 52 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
| STM32L4A6VGT6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
| STM32L4A6VGY6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | WLCSP100 | 83 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
| STM32L4A6QGI6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | UFBGA132 | 110 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 19 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
| STM32L4A6ZGT6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | LQFP144 | 115 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
| STM32L4A6AGI6 | 80 | ARM Cortex-M4 | 1024 | 320 | 0 | UFBGA169 | 136 | 1.71 | 3.6 | 11 | 2 | 2 | 2 | 0 | 3 | 24 | 0 | 0 | 2 | 2 | 2 | 3 | 1 | 0 | 4 | [4] | 3+2 | 1 | 2 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 8x40/4x44 | 0 | 0 | 2 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |

STM32 L4+系列 – ARM® Cortex®-M4超低功耗高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Erpmem (Bytes) | Package Name | IOInb | Vmin | Vmax | Nb Timer (16-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Channels | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | Nb DAC 16-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/DES | SHA | HMxAC | |
|---|-----------------|---------------|----------------|--------------|----------------|--------------|-------|------|------|--------------------|-------------------|--------------------------------|--------------------|--------------------------|------------------------|------------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|---------|-----|-------|---|
| STM32L4R5/4S5系列：带USB OTG - 120 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L4R5AI6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA169 | 140 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4R5QG16 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | UFBGA132 | 110 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4R5QI16 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA132 | 110 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4R5VGT6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4R5VIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4R5ZGT6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | LQFP144 | 115 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | |
| STM32L4R5ZIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP144 | 115 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | |
| STM32L4R5ZGY6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | WLCSP144 | 115 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4R5ZIY6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | WLCSP144 | 115 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | |
| STM32L4R5AGI6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | UFBGA169 | 140 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4S5AI6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA169 | 140 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 |
| STM32L4S5QI16 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA132 | 110 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | |
| STM32L4S5VIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | |
| STM32L4S5ZIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP144 | 115 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | |
| STM32L4S5ZIY6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | WLCSP144 | 115 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | |
| STM32L4R7/4S7系列：带USB OTG与TFT接口 - 120 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L4R7AI6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA169 | 140 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4R7VIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| STM32L4R7ZIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP144 | 115 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |

STM32 L4+系列 – ARM® Cortex®-M4超低功耗高性能MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | Eprom (Bytes) | Package Name | IONb | Vmin | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb Low Power Timer | Nb High Resolution Timer | Nb ADC 12-bit Cell | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb ADC 16-bit Channels | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | U(S)ART | LPUART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMACE | |
|---|-----------------|---------------|----------------|--------------|---------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------------------------|------|-------|-----|----------|-----|-----|--------|---------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|---------|--------|-----|---------|-------|------|-------|------|-----|----------|-----|-------|---|
| STM32L4S7AI6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA169 | 140 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| STM32L4S7VIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP100 | 83 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| STM32L4S7ZIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP144 | 115 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| STM32L4R9/4S9系列：带USB OTG与MIPI-DSI接口 - 120 Mhz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32L4R9VGT6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | LQFP100 | 77 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9VIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP100 | 77 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9ZGT6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | LQFP144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9ZIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9ZJG6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | UFBGA144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9ZJL6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9ZGY6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | WLCSP144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9ZIY6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | WLCSP144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9AGI6 | 120 | ARM Cortex-M4 | 1024 | 640 | 0 | UFBGA169 | 131 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4R9AI6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA169 | 131 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | |
| STM32L4S9VIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP100 | 77 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| STM32L4S9ZIT6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | LQFP144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| STM32L4S9ZJL6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| STM32L4S9ZIY6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | WLCSP144 | 112 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 16 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| STM32L4S9AI6 | 120 | ARM Cortex-M4 | 2048 | 640 | 0 | UFBGA169 | 131 | 1.71 | 3.6 | 9 | 2 | 2 | 2 | 0 | 1 | 14 | 0 | 0 | 2 | 2 | 2 | 3 | 2 | 0 | 4 | [4] | 3+2 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 2 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |

STM32 WB系列 – ARM® Cortex® -M4和Cortex® -M0+双核无线MCU

| Commercial Product Code | Frequency (MHz) | Core | Flash (Kbytes) | Ram (Kbytes) | E2prom (Bytes) | Package Name | IONb | Vinb | Vmax | Nb Timer (6-bits) | Nb Timer (32-bit) | Nb Motor Control Timer(16bits) | Nb High Resolution Timer | Nb Low Power Timer | Nb ADC 12-bit Channels | Nb ADC 16-bit Cell | Nb DAC 12-bit Channels | COMP | OPAMP | SPI | Quad SPI | I2S | I2C | FMPI2C | USART | LPuART | CAN | SDIO | FSMC | FMC | USB Device | USB OTG_FS | USB OTG_HS | Ethernet | MDIOS | Segment LCD | TFT LCD | DSiHOST | SAI | SPDIFRX | DFSDM | DCMI | SWPMI | TRNG | AES | DES/TDES | SHA | HMAC | | |
|------------------------------|-----------------|------------------------------|----------------|--------------|----------------|--------------|------|------|------|-------------------|-------------------|--------------------------------|--------------------------|--------------------|------------------------|--------------------|------------------------|------|-------|-----|----------|-----|-----|--------|-------|--------|-----|------|------|-----|------------|------------|------------|----------|-------|-------------|--------------|---------|-----|---------|-------|------|-------|------|-----|----------|-----|------|---|---|
| STM32WBx5 系列 - 64 MHz | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| STM32WB55CCU6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 256 | 256 | 0 | UFQFPN48 | 30 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 13 | 0 | 2 | 0 | 1 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 4x13 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
| STM32WB55CEU6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 512 | 256 | 0 | UFQFPN48 | 30 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 13 | 0 | 2 | 0 | 1 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 4x13 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |
| STM32WB55CGU6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 1024 | 256 | 0 | UFQFPN48 | 30 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 13 | 0 | 2 | 0 | 1 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 4x13 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32WB55RCV6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 256 | 256 | 0 | VFQFPN68 | 49 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 19 | 0 | 2 | 0 | 2 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 7x23 or 4x26 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32WB55REV6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 512 | 256 | 0 | VFQFPN68 | 49 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 19 | 0 | 2 | 0 | 2 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 7x23 or 4x26 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32WB55RGV6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 1024 | 256 | 0 | VFQFPN68 | 49 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 19 | 0 | 2 | 0 | 2 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 7x23 or 4x26 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32WB55VCY6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 256 | 256 | 0 | WLCSP100 | 72 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 19 | 0 | 2 | 0 | 2 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x40 or 4x44 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32WB55VEY6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 512 | 256 | 0 | WLCSP100 | 72 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 19 | 0 | 2 | 0 | 2 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x40 or 4x44 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |
| STM32WB55VGY6 | 64 | ARM Cortex-M4 ARM Cortex-M0+ | 1024 | 256 | 0 | WLCSP100 | 72 | 1.71 | 3.6 | 3 | 1 | 1 | 2 | 0 | 0 | 1 | 19 | 0 | 2 | 0 | 2 | 1 | 0 | 2 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 8x40 or 4x44 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 0 | |

缩写和封装

缩写

| | | | | | |
|------------------|--|-------|-------------------------------------|-------|---|
| ADC | : Analog-to-digital converter | LCD | : Liquid crystal display | SPI | : Serial peripheral interface |
| ART | : Auto-reload timer | LIN | : Local interconnect network | SSC | : Single-cycle switching support |
| ATAPI | : AT attachment packet interface | LVD | : Low voltage detection | SSP | : Synchronous serial port |
| AWU | : Auto wake-up from halt | MAC | : Multiply accumulator | TBU | : Time base unit |
| BLPD | : Byte level protocol decoder | MC | : Motor control | TLI | : Top level interrupt |
| BOD | : Brown-out detector | MFT | : Multifunction timer | UART | : Universal asynchronous receiver transmitter |
| CAN | : Controller area network | MMC | : MultiMediaCard | USART | : Universal sync/async receiver transmitter |
| CAPCOM | : Capture compare | NMI | : Non-maskable interrupt | USB | : Universal Serial Bus |
| CSS | : Clock security system | OSG | : Oscillator safeguard | WDG | : Watchdog timer |
| DALI | : Digital addressable lighting interface | PCA | : Programmable counter array | WWDG | : Window watchdog timer |
| DDC | : Data display channel | PDR | : Power-down reset | | |
| DiSEqC | : Digital satellite equipment control | PHW | : Programmable halt wake-up | | |
| DMA | : Direct memory access | PEC | : Peripheral event controller | | |
| DSC | : Dual supply control | PLD | : Programmable logic device | | |
| DTC | : Data transfer coprocessor | PLL | : Phase locked loop | | |
| ETM | : Embedded trace macrocell | POR | : Power-on reset | | |
| EMI | : External memory interface | PVD | : Programmable voltage detector | | |
| HDLC | : High-level data link control | PVR | : Programmable voltage regulator | | |
| IAP | : In-application programming | PWM | : Pulse width modulation | | |
| IC/OC | : Input capture/output compare | ROP | : Readout protection | | |
| ICP | : programming | RTC | : Real-time clock timer | | |
| IR | : Infrared | SAI | : Serial Audio Interface | | |
| IrDA | : Infrared data association | SC | : Smartcard | | |
| ISP | : In-situ programming | SCI | : Serial communication interface | | |
| I ² C | : Inter-integrated circuit | SCR | : Smartcard reader | | |
| I ² S | : Inter-IC sound | SDIO | : Secure digital input output | | |
| | | SDMMC | : Secure Digital / Multi Media Card | | |
| | | SMI | : Serial memory interface | | |

封装

| | |
|-------------|---------------------------------------|
| DIP | : Dual in-line package |
| LCC | : Leaded chip carrier |
| PDIP Shrink | : Shrink Plastic Dual In-line Package |
| PQFP | : Plastic quad flat package |
| SO | : Small outline |
| LQFP | : Low-profile quad flat package |
| PBGA | : Plastic ball grid array |
| DFN | : Dual flat no-lead |
| QFN | : Quad flat no-lead |
| WLCSP | : Wafer-Level Chip-Scale Package |

STM32 & STM8产品型号



家族
STM32 32位MCU
STM8 8位MCU

产品类别
A 汽车级
F 基础型
L 超低功耗
S 标准型
T 触摸感应
WB 无线产品
xP Fastrom

特定功能 (3位数字)
(依据产品系列非详细列表)
STM32x ...
051 入门级
103 STM32基础型
303 103升级版, 带DSP和模拟外设
407 高性能, 带DSP和FPU
152 超低功耗
STM8x .../STM8Ax...
103 主流入门级
F52 汽车级CAN
L31 低端汽车级

引脚数(适用于STM8和STM32)

| | | |
|---------------|-------------|------------------------|
| D 14引脚 | C 48 & 49引脚 | A 169引脚 |
| Y 20引脚(STM8) | U 63引脚 | I 176 & 201 (176+25)引脚 |
| F 20引脚(STM32) | R 64 & 66引脚 | B 208引脚 |
| E 24 & 25引脚 | J 72引脚 | N 216引脚 |
| G 28引脚 | M 80引脚 | X 256引脚 |
| K 32引脚 | O 90引脚 | 汽车级 |
| T 36引脚 | V 100引脚 | 8 48 |
| H 40引脚 | Q 132引脚 | 9 64 |
| S 44引脚 | Z 144引脚 | A 80 |

闪存容量(Kbytes)

| | |
|---|------------|
| 0 | 1 |
| 1 | 2 |
| 2 | 4 |
| 3 | 8 |
| 4 | 16 |
| 5 | 24 |
| 6 | 32 |
| 7 | 48 |
| 8 | 64 |
| 9 | 72 |
| A | 96 or 128* |
| B | 128 |
| Z | 192 |
| C | 256 |
| D | 384 |
| E | 512 |
| F | 768 |
| G | 1024 |
| H | 1536 |
| I | 2048 |

Note:
* 仅针对STM8A

封装
B Plastic DIP*
D Ceramic DIP*
G Ceramic QFP
H LFBGA /TFBGA
I UFBGA Pitch 0.5**
J UFBGA Pitch 0.8**
K UFBGA Pitch 0.65**
M Plastic SO
P TSSOP
Q Plastic QFP
T QFP
U UFQFPN
V VFQFPN
Y WLCSP
* Dual-in-Line封装
** 仅针对全新产品系列
现有产品系列请使用H

温度范围(°C)
6和A - 40到+ 85
7和B - 40到+ 105
3和C - 40到+ 125
D - 40到+ 150

固件版税
U Universal 不用于生产(样品和工具)
V MP3解码器
W MP3编解码器
J 0.80 mm
D IS2T JAVA

选项
xxx Fastrom code
or
xTR Tape and Reel
Dxx No RTC (STM8L)
Dxx BOR OFF with Special bonding + Boot standard
Dxx BOR OFF with Boot I2CS (Special)
Sxx BOR OFF
lxx BOR ON
No Letter BOR ON + Boot standard
or
Yxx Die rev (Y)

ST MCU Finder

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STM32单片机



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